

● PRINTER RUSH ●

(PTO ASSISTANCE)

Application : <u>09586557</u>	Examiner : <u>LEVITAN</u>	GAU : <u>2662</u>
From: <u>NPB</u>	Location: <u>IDC</u> FMF FDC	Date: <u>02/06/06</u>

Tracking #: epm 09586557 Week Date: 12/26/05

DOC CODE	DOC DATE	MISCELLANEOUS
<input type="checkbox"/> 1449		<input type="checkbox"/> Continuing Data
<input type="checkbox"/> IDS		<input type="checkbox"/> Foreign Priority
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<input type="checkbox"/> SRFW		<input type="checkbox"/> Other
<input checked="" type="checkbox"/> DRW	<u>05/31/00</u>	
<input type="checkbox"/> OATH		
<input type="checkbox"/> 312		
<input type="checkbox"/> SPEC		

ATTN: CHIEF DRAFTSPERSON OF THE OFFICE OF PATENT PUBLICATION

[RUSH] MESSAGE:

Please provide replacement drawing sheet for Figure 2.
(Letter "s" was added in the drawing sheet dated 05/31/00 for
Figure 2 in the word "system").

Thank you

[XRUSH] RESPONSE: 2/15/06

DRAWING CORRECTED

INITIALS: LAM

NOTE: This form will be included as part of the official USPTO record, with the Response document coded as XRUSH.

REV 10/04

The diagram illustrates a network architecture. At the top, four components are connected to a central horizontal line representing the Local Area Network (LAN). From left to right, these components are: Client Terminal (12), Client Terminal (14), Multi-Point Control Unit (16), and Gateway (18). Each component is connected to the LAN line by a double-headed vertical arrow. Below the LAN line, two more components are connected: Gatekeeper (20) on the left and Server (22) on the right, also connected by double-headed vertical arrows. The entire system is labeled with the reference numeral 10 in the top left corner. The LAN line itself is labeled 24.

The diagram illustrates a hierarchical system architecture across three levels:

- Network Level 30:** Contains a **Server** and a **Standby Server** (dashed circle) connected by a bidirectional dashed arrow.
- System Level 32:** Contains five nodes: **Gate-keeper**, **Client 1**, **Client 2**, **MCU**, and **Gate-way**. These nodes are connected to the **Server** at the Network Level.
- Component Level 34:** Shows the internal components of the System Level nodes:
 - Gate-keeper** is connected to **I/O Card** and **CPU**.
 - Client 1** is connected to **CPU**.
 - Client 2** is connected to **CPU** and **Standby CPU** (dashed circle).
 - MCU** is connected to **CPU**.
 - Gate-way** is connected to **CPU**.

FIG. 2